

IN THE CLAIMS

Amend the claims as follows:

1.(Amended) A rectifier, comprising:

~~B3~~  
a reference primary circuit;

a transformer having a primary side connected to said reference primary circuit and having a secondary side;

~~Sub C1~~  
first and second rectifiers in synchronous connection at said secondary side, said first and second rectifiers each having at least three leads, one of said three leads being a control lead;

first and second clamping transistors, said first clamping transistor being connected between said control lead of said first rectifier and said secondary side, said second clamping transistor being connected between said control lead of said second rectifier and said secondary side; and

a fixed voltage source connected to control leads of said first and second transistors.

✓  
Cancel claim 4 without prejudice.

5. (Amended) A rectifier as claimed in claim 1, further comprising:

~~B4~~  
an output;

a first filter element connected between said first rectifier and said output; and

a second filter element connected between said output and ground.

B5

5N4  
C3

11.(Amended) A self-driven synchronous rectifier, comprising:

- a transformer having a primary and seconding winding;
- an input for an input voltage connected to said primary winding;
- a pair of rectifiers connected in a synchronous connection, a first rectifier of said pair of rectifiers including a source drain connection in series with a first lead of said secondary winding and a gate connected to a second lead of said secondary winding;
- a second rectifier of said pair of rectifiers having a source and drain leads connected across said first and second leads of said secondary winding and a gate connected to said first lead of said secondary winding;
- a first transistor connected between said gate of said first rectifier and said second lead of said secondary winding;
- a second transistor connected between said gate of said second rectifier and said first lead of said secondary winding; and
- a voltage source connected to gates of said first and second transistors.